GOA UNIVERSITY

Taleigao Plateau, Goa 403 206

RESULT REGISTER FOR M.E ELECTRONICS & TELECOMMUNICATION (MICROELECTRONICS) SEMESTER - I EXAMINATION HELD IN MAY 2018 COURSE : Revised Course - 2013

COLLE	GE: GOAC	OLLEGE OF E	ENGINEERING	G									
Seat No	: 3101	P R No :	201209938	Se	x: M		Nam	ne :	AMBALLOC	R ROS	HUN GE	ORGE	,
No Of A	ttempts : 2				No Of Credits	5	Gra Obtai		SGP	A			
	Solid State	Devices and S	emiconductor	Physics									
				Theory	4		BC	+					
				IA	2		BB	+					
	Digital VLSI	Design											
				Theory	4		BC	+					
				IA	2		BC	+					
	Analog VLSI	Design											
				Theory	4		CC	+					
				IA	2		BC	+					
	VLSI Techno	ology											
				Theory	4		BB	+					
				IA	2		BB	+					
	Advanced E	ngineering Ma	thematics										
				Theory	4		BC	Ρ					
				IA	2		CC	+					
	HDL - LAB												
				IA	2		AA	+					
				Practical	2		AB	+					
	VLSI CAD -	LAB											
				IA	2		BB	+					
				Practical	2		BB	+					
				Total :	38				6.42 PAS	2 P SES		*****	

Grade	Grade Points	Perform	ance	Read By : phile
AO	10	Outstan	ding	
AA	9	Excelle	ent	Checked by
AB	8	Very Go	bod	Data: 1010
BB	7	Good	ł	Date: 1/8/18
BC	6	Fair		1
CC	5	Satisfac	tory	
FF	0	Fail		

Aniketh Gaonkar Assistant Registrar-E(Proff.)

Prof. Anuradha Wagle Controller Of Examinations

Prof. Y. V. I Registra



P: Passes; F: Fails; A/ABS: Absent; N/NAP: Non Appearance; X/NE: Not Eligible; +: Grades Carried Over; SGPA: Semester Grade Point Average; CGPA: Cummulative Grade Point Average

Discrepancy, if any in the Result shall be informed to the University within a week from the date of receipt of the result by the college Page 1 of 1

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